

ABSTRACT OF THE DISCLOSURE

The present invention provides an image processing circuit having a D/A converter that converts input image data D_a to an analog signal to generate an image signal VID. The output range of the D/A converter is controlled by an output-range control signal CTLout having a different signal level according to the type of a liquid-crystal display panel used. Therefore, a range where the signal level of the image signal VID is changed can be adjusted according to the type of the liquid-crystal display panel. Consequently, even when an image-signal processing circuit is used with one of a plurality of types of liquid-crystal display panels having different V-T characteristics, since the data values of input image data D_a can be assigned to a desired applied-voltage range, high-definition images can be displayed.

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